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In Re the Application of:

Inventor(s): Osamu S. Nakagawa Confirmation No.: 3635
Serial No.: 09/891,324 Examiner: Schillinger, Laura M.
Filed: June 27, 2001 Group Art Unit: 2813
Title: PROCESS FOR HIGH-DIELECTRIC CONSTANT METAL-INSULATOR
METAL CAPACITOR IN VLSI MULTI-LEVEL METALIZATION SYSTEMS

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Commissioner for Patents
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Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TO THE USPTO

I hereby certify that this correspondence is being transmitted to the Patent and Trademark Office facsimile number (571) 273-8300 on August 19, 2005. This correspondence contains the following document(s):

1 sheet of Notice of Appeal From the Examiner to the Board of Patent Appeals and Interferences. (2 copies).
5 sheets of Pre-Appeal Brief Request for Review.


Respectfully submitted,

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August 19, 2005


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